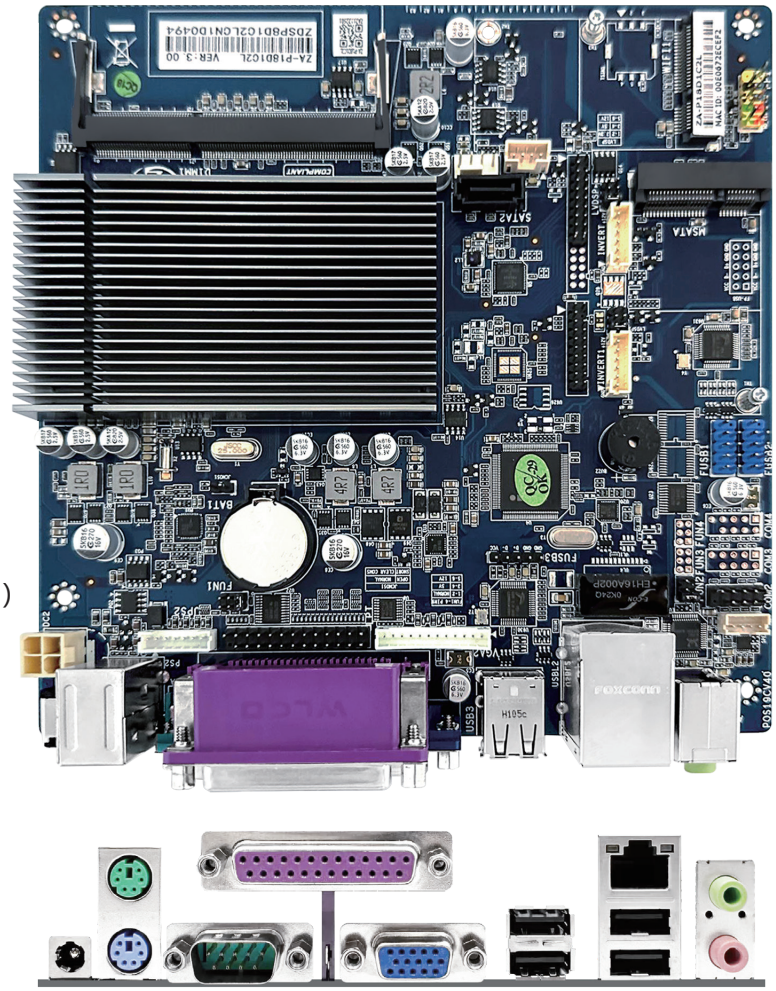
ZA-P18D1C2L Motherboard

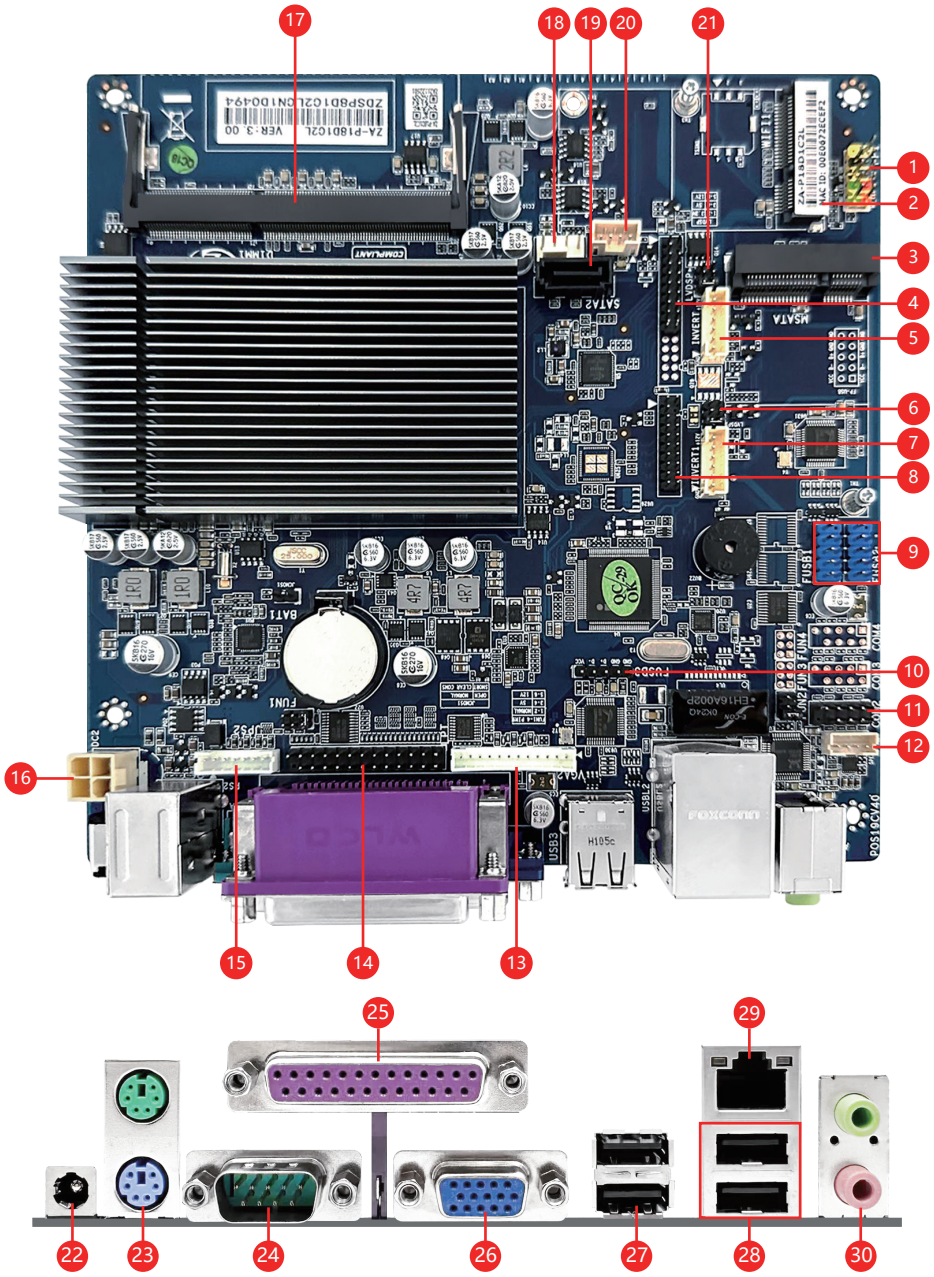


Product characteristics:

- Intel® Celeron® J1800/J1900 CPU
- Support 2*LVDS/2*EDP*/1*VGA Displays the output
- 1*Serial port header(It can support 3 built-in serial port headers)
- Support 9*USB (4*USB2.0 interface/5*USB2.0 Pin)

ZA-P18D1C2L Motherboard Specifications

Size	170mm*170mm
CPU	Intel® Celeron® J1800/J1900 CPU
RAM	Support 1*SO-DIMM DDR3L 1333MHz
BIOS	AMIBIOS 64Mbit Flash
Internal I/O	1*Front Panel Pin 2*LVDS/EDP Pin 2*Screen supply voltage selection jumper 3*FUSB2.0 Pin(Support 5*USB2.0 interface) 1*COM Pin 1*Speaker Pin Header (2W/CH) 1*VGA Pin 1*LPT Pin 1*PS/2 Pin 1*4Pin Power Outlet 1*CPU_FAN Pin 1*SATA Data Interface 1*JSATA Power Supply Interface
Rear I/O	1*DC 12V interface 1*PS/2 interface 1*COM interface 1*LPT interface 1*VGA interface 4*USB2.0 interface 1*LAN interface 2*AUDIO interface (1*LINE OUT, 1*MIC)
LAN	1*Realtek 8106; 100Mbps/10Mbps Ethernet Controller
Expansion slot	1*Mini pcie slot(MSATA)/1*Mini pcie slot WIFI(PCIE/USB))
Operating System	Windows7/10, Linux



1	Front panel Pin
2	Mini pcie slot（WIFI）
3	Mini pcie slot（MSATA）
4	LVDS/EDP Pin
5	Inverter Pin
6	LVDSP jumper
7	Inverter1 Pin
8	LVDS/EDP Pin
9	FUSB1/2 Pin
10	FUSB3 Pin
11	COM2 Pin
12	Amplifier speaker connector pin(2W/CH)
13	VGA Pin
14	LPT Pin
15	PS/2 Pin
16	4Pin Power Outlet
17	DDR3 slot
18	CPU_FAN Pin
19	SATA Data interface
20	JSATA Power Supply interface
21	LVDSP jumper

22	DC 12V interface	27	USB2.0 interface
23	PS/2 interface	28	LAN interface
24	COM interface	29	USB2.0 interface
25	LPT interface	30	AUDIO interface（1*LINE OUT、1*MIC）
26	VGA interface		

ZA-P18D1C2L Mainboard Definition

(1) Front panel Pin

Location	Pin	Definition	Pin	Definition
FP1 (2.54mm)	1	3.3V	2	Power LED
	3	Hard Disk Drive LED	4	GND
	5	Reset Switch	6	Power Switch
	7	GND	8	GND
	9	NC		

(4/8) LVDS/EDP Pin

Location	Pin	Definition	Pin	Definition	Pin	Definition	NOTE
EDP_LVDS1 (2mm)	1	EDP_CONN_AUXN	2	EDP_AUXN	2	EDP_LVDS_AUXN	This jumper is used to select between the LVDS1 and eDP1 signals. If you need to use the eDP signal, place the jumper on the left side, in the (1-2) position. Conversely, if you need to use the LVDS signal, place the jumper on the right side, in the (2-3) position.
	4	EDP_CONN_AUXP	5	EDP_AUXP	5	EDP_LVDS_AUXP	
	7	EDP_CONN_TXP0	8	EDP_TXP0	8	EDP_LVDS_TXP0	
	10	EDP_CONN_TXN0	11	EDP_TXN0	11	EDP_LVDS_TXN0	
	13	EDP_CONN_TXP1	14	EDP_TXP1	14	EDP_LVDS_TXP1	
	16	EDP_CONN_TXN1	17	EDP_TXN1	17	EDP_LVDS_TXN1	
	19	EDP_CONN_HPD	20	EDP_HPD	20	EDP_LVDS_HPD	

Location	Pin	Definition	Pin	Definition	Pin	Definition	NOTE
EDP_LVDS2 (2mm)	1	EDP_CONN_AUXN	2	EDP_AUXN	2	EDP_LVDS_AUXN	This jumper is used to select between the LVDS1 and eDP1 signals. If you need to use the eDP signal, place the jumper on the left side, in the (1-2) position. Conversely, if you need to use the LVDS signal, place the jumper on the right side, in the (2-3) position.
	4	EDP_CONN_AUXP	5	EDP_AUXP	5	EDP_LVDS_AUXP	
	7	EDP_CONN_TXP0	8	EDP_TXP0	8	EDP_LVDS_TXP0	
	10	EDP_CONN_TXN0	11	EDP_TXN0	11	EDP_LVDS_TXN0	
	13	EDP_CONN_TXP1	14	EDP_TXP1	14	EDP_LVDS_TXP1	
	16	EDP_CONN_TXN1	17	EDP_TXN1	17	EDP_LVDS_TXN1	
	19	EDP_CONN_HPD	20	EDP_HPD	20	EDP_LVDS_HPD	

(9/10) FUSB Pin

Location	Pin	Definition	Pin	Definition
FUSB1 (2.54mm)	1	5V_RUN	2	5V_RUN
	3	USB_PN4	4	USB_PN5
	5	USB_PP4	6	USB_PP5
	7	GND	8	GND
	9	NC	10	GND

Location	Pin	Definition	Pin	Definition
FUSB2 (2.54mm)	1	5V_RUN	2	5V_RUN
	3	USB_PN4	4	USB_PN5
	5	USB_PP4	6	USB_PP5
	7	GND	8	GND
	9	NC	10	GND

Location	Pin	Definition
FUSB3 (2.54mm)	1	VCC
	2	D-
	3	D+
	4	GND
	5	GND

(11) COM Pin

Location	Pin	Definition	Pin	Definition
COM2 (2.54mm)	1	2DCD	2	2SIN
	3	2SOUT	4	2DTR
	5	GND	6	2DSR
	7	2RTS	8	2CTS
	9	2RI		

(14) LPT Pin

Location	Pin	Definition	Pin	Definition
LPT (2mm)	1	P_STB_R	2	P_AFD_R
	3	P_PD0_R	4	ERR-
	5	P_PD1_R	6	P_INIT_R
	7	P_PD2_R	8	P_SLIN_R
	9	P_PD3_R	10	GND
	11	P_PD4_R	12	GND
	13	P_PD5_R	14	GND
	15	P_PD6_R	16	GND
	17	P_PD7_R	18	GND
	19	ACK-	20	GND
	21	BUSY	22	GND
	23	PE	24	GND
	25	SLCT		

(18) CPU_FAN Pin

Location	Pin	Definition
CPU_FAN1	1	GND
	2	+12V
	3	FAN_TAC
	4	FAN_CTL