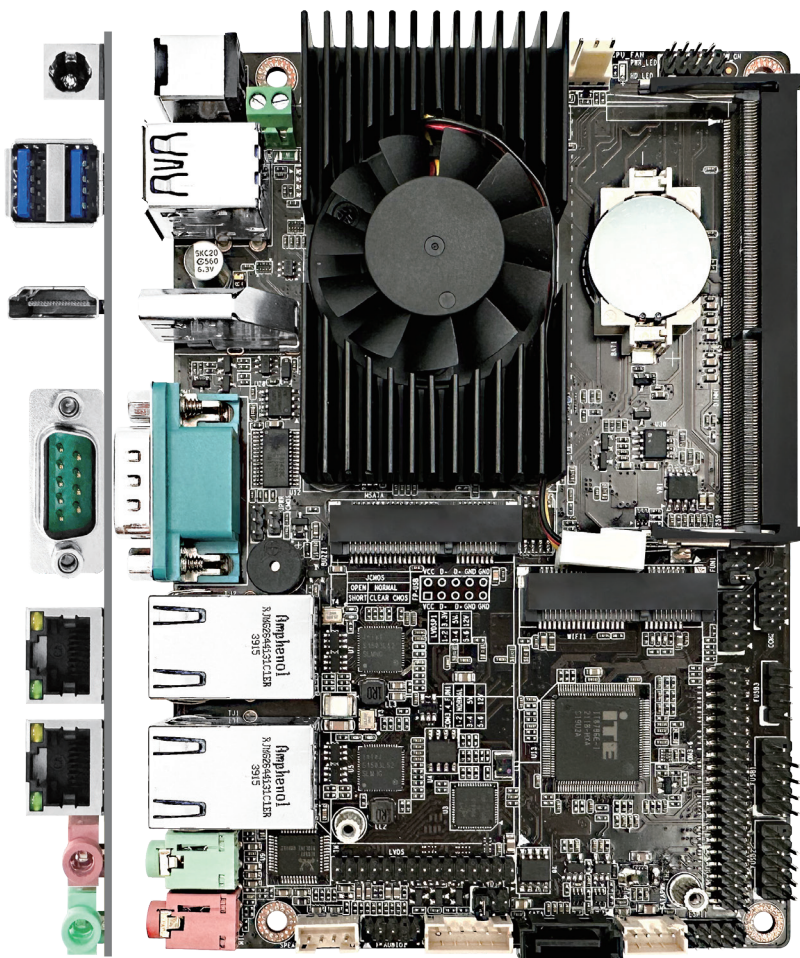


JPL35AV10主板

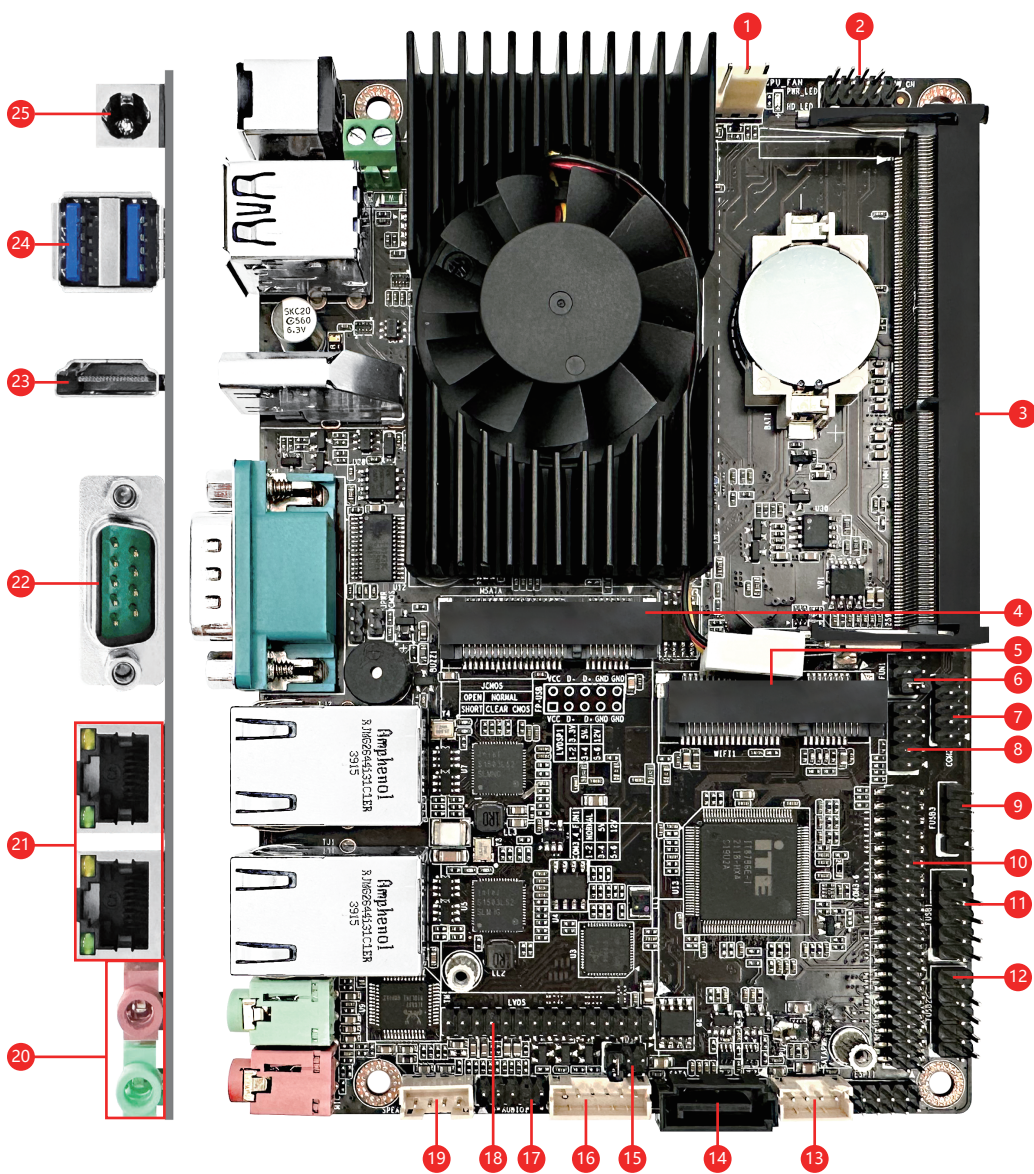
产品特性:

- 英特尔® Jasper lake N5105
- 支持2个LAN口(Intel i225; 2.5Gbps/1Gbps/100Mbps/10Mbps以太网控制器)
- 支持1*EDP/1*LVDS/1*HDMI显示输出
- 支持7个USB (2*USB3.0接口/5*USB2.0排针)



JPL35AV10主板规格

板型尺寸	148mm*102mm
处理器	英特尔® Jasper lake N5105
内存	支持1个DDR4 2933MHz, 最大支持16GB
BIOS	AMIBIOS 128Mbit 闪存
内置I/O接口	1*CPU FAN排针 1*Front panel排针 1*COM FUN跳线 1*COM排针 1*GPIO排针 3*FUSB排针(支持5个USB2.0接口) 1*COM3-6排针 1*JSATA供电接口 1*SATA数据接口 1*屏供电电压选择跳线 1*Inverter排针 1*AUDIO排针 1*LVDS排针 1*功放喇叭排针(2W/CH)
外置I/O接口	1*DC 12V接口 2*USB3.0接口 1*HDMI接口 1*COM口 2*LAN接口 2*AUDIO接口 (1*LINE OUT、1*MIC)
集成网卡	2*Intel I225; 2.5Gbps/1Gbps/100Mbps/10Mbps以太网控制器
扩展槽/口	1*Mini pcie插槽 (WIFI) /1*Mini pcie插槽 (MSATA)
操作系统	Windows10/11; Linux



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1

CPU FAN排针

2

Front panel排针

3

DDR4内存槽

4

Mini pcie插槽 (WIFI)

5

Mini pcie插槽 (MSATA)

6

COM FUN跳线

7

COM口排针

8

GPIO排针

9

FUSB1排针

10

COM3-6排针

11

FUSB2排针

12

FUSB3排针

13

JSATA供电接口

14

SATA数据接口

15

屏供电电压选择跳线

16

Inverter排针接口

17

音频输出排针

18

LVDS排针

19

功放喇叭排针(2W/CH)

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AUDIO接口 (1*LINE OUT、1*MIC)

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LAN口

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COM口

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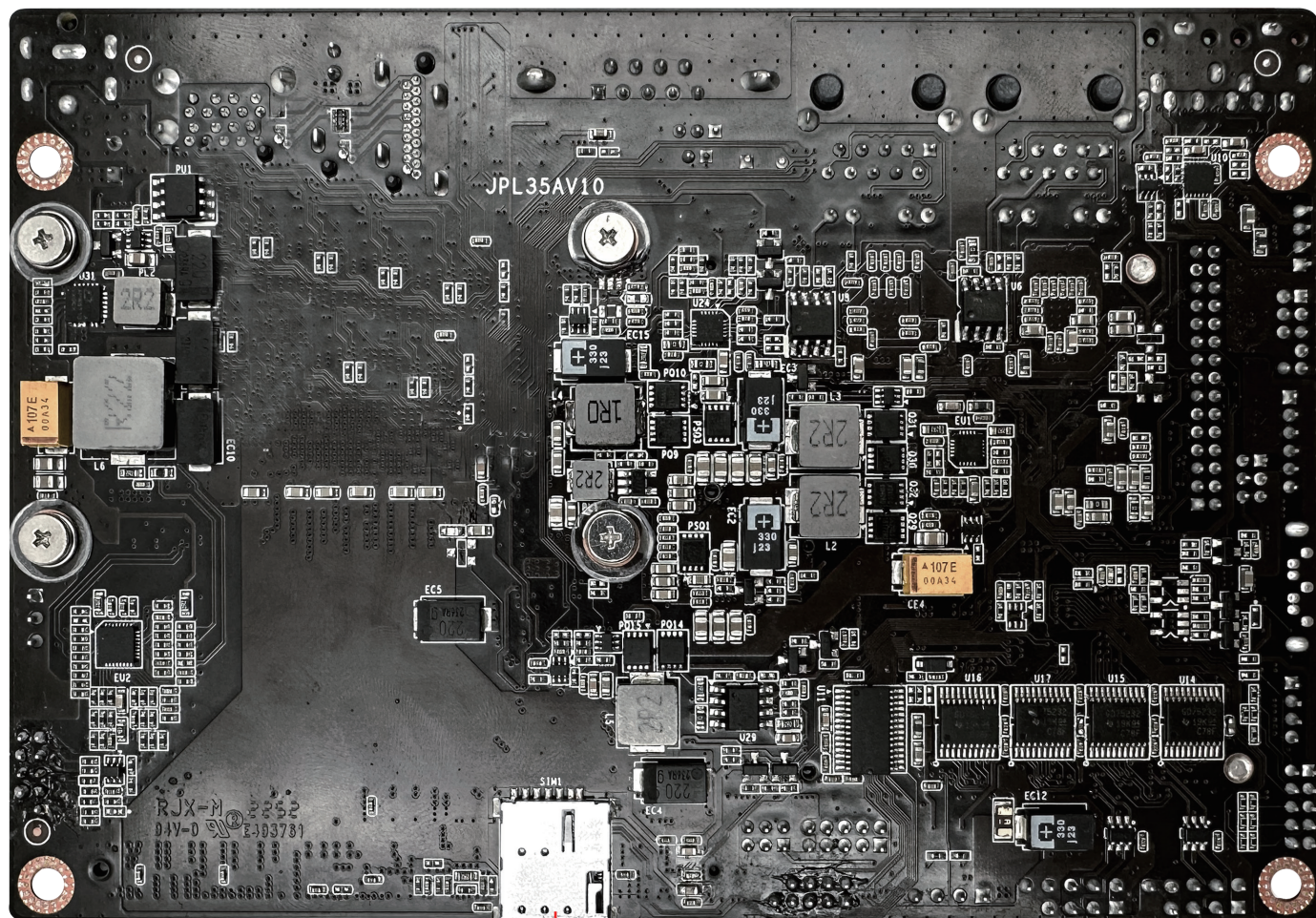
HDMI接口

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USB3.0接口

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DC 12V接口



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SIM卡槽

JPL35AV10主板定义

(1) CPU_FAN排针

Location	Pin	Definition
CPU_FAN1	1	GND
	2	+12V
	3	FAN_TAC
	4	FAN_CTL

(2) Front panel排针

Location	Pin	Definition	Pin	Definition
FP1 (2mm)	1	3.3V	2	Power LED
	3	Hard Disk Drive LED	4	GND
	5	Reset Switch	6	Power Switch
	7	GND	8	GND
	9	NC		

(6) COM FUN跳线

Location	Pin	Definition	Pin	Definition	NOTE
FUN1 (2mm)	1	5RI	2	5RI-	此插针的功能为COM36供电选择,默认为不供电即跳帽置(1-2)
	3	5RI	4	5V_RUN	
	5	5RI	6	12V_RUN	

(7/10) COM排针

Location	Pin	Definition	Pin	Definition
COM2 (2mm)	1	2DCD	2	2SIN
	3	2SOUT	4	2DTR
	5	GND	6	2DSR
	7	2RTS	8	2CTS
	9	2RI		

Location	Pin	Definition	Pin	Definition
COM36 (2mm)	1	3DCD	2	3SIN
	3	3SOUT	4	3DTR
	5	GND	6	3DSR
	7	3RTS	8	3CTS
	9	3RI	10	NC
	11	4DCD	12	4SIN
	13	4SOUT	14	4DTR
	15	GND	16	4DSR
	17	4RTS	18	4CTS
	19	4RI	20	NC
	21	5DCD	22	5SIN
	23	5SOUT	24	5DTR
	25	GND	26	5DSR
	27	5RTS	28	5CTS
	29	5RI	30	NC
	31	6DCD	32	6SIN
	33	6SOUT	34	6DTR
	35	GND	36	6DSR
	37	6RTS	38	6CTS
	39	6RI	40	NC

(8) GPIO排针

Location	Pin	Definition	Pin	Definition
GPIO (2mm)	1	GP23_IO	2	3V_RUN
	3	GP30_IO	4	5V_RUN
	5	GND	6	12V_RUN

(9/11/12) FUSB排针

Location	Pin	Definition	Pin	Definition
FUSB1 (2.54mm)	1	5V_RUN	2	5V_RUN
	3	USB_PN4	4	USB_PN5
	5	USB_PP4	6	USB_PP5
	7	GND	8	GND
	9	NC	10	GND

Location	Pin	Definition
FUSB3 (2.54mm)	1	5V_RUN
	2	FDM7
	3	FDP7
	4	GND

Location	Pin	Definition	Pin	Definition
FUSB2 (2.54mm)	1	5V_RUN	2	5V_RUN
	3	FDM1	4	FDM2
	5	FDP1	6	FDP2
	7	GND	8	GND
	9	NC	10	GND

(18) LVDS排针

Location	Pin	Definition	Pin	Definition
LVDS (2mm)	1	VCC	2	VCC
	3	VCC	4	GND
	5	GND	6	HPD
	7	LVDS_TXN0/EDP_TXN0	8	LVDS_TXP0/EDP_TXP0
	9	LVDS_TXN1/EDP_TXN1	10	LVDS_TXP1/EDP_TXP1
	11	LVDS_TXN2	12	LVDS_TXP2
	13	GND	14	GND
	15	LVDS_CLKN/EDP_AUXN	16	LVDS_CLKP/EDP_AUXP
	17	LVDS_TXN3	18	LVDS_TXP3
	19	LVDS1_TXN0	20	LVDS1_TXP0
	21	LVDS1_TXN1	22	LVDS1_TXP1
	23	LVDS1_TXN2	24	LVDS1_TXP2
	25	GND	26	GND
	27	LVDS1_CLKN	28	LVDS1_CLKP
	29	LVDS1_TXN3	30	LVDS1_TXP3